

Applicant : Gilbert Wolrich et al.
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Attorney's Docket No.: 10559-614001 / Intel Corporation P12853

Amendments to the Drawings:

The attached replacement sheets of formal drawings replace the original sheets submitted by the applicant in the instant application. The formal drawings include changes to Fig. 3B.

In Figure 3B, the notation “.GT.” that appeared in elements 126 and 129 of the flowchart was replaced with the wording “GREATER THAN”.

Attachments following last page of this Amendment:

Replacement Sheet (4 pages)

Annotated Sheet Showing Change(s) (1 pages)

REMARKS

Claims 1, 3-9, 11-17, 19-25, and 27-31 are pending. Claims 1, 9, 17, and 25 are independent.

Applicant filed a Supplemental Information Disclosure Statement on January 17, 2006, along with a Request for Initialed PTO Form 1449 for the Information Disclosure Statement filed on June 24, 2004. Applicant respectfully requests consideration of all the references cited therein.

Applicant amended FIG. 3B to replace, for greater clarity, the notation “.GT.” that appeared in the flowchart with the wording “GREATER THAN”. Applicant notes that page 7, lines 14-19 of the originally filed application describes, in relation to element 126 of FIG. 3B, that “[p]rocess 120 determines (126) whether the cell count from the head BDA is greater than zero,” and further describes, in relation to element 129 of FIG. 3B, that “[i]f the cell count is not greater than zero, process 120 determines (129) if the Queue Count is greater than or equal to one.”

Applicant amended for greater clarity independent claim 1 to replace the wording “queue entries”, appearing in the preamble, with “queues”, replace the wording “queue entry” with “queue”, and remove the word “first” that preceded the word “queue”. Applicant also amended independent claim 1 to remove the wording “one of” that preceded the wording “a plurality of data buffers” to clarify that the various stored addresses correspond to a plurality of data buffers, as described, for example, in FIGS. 2A and 2B and in page 5, line 10, to page 6, line 13, of the Specification. Applicant additionally amended independent claim 1 to replace the word “the”, that preceded “first BDA entry”, with “a” to form a proper antecedent basis.

Applicant further amended independent claim 1 to remove the word “circular” that preceded the wording “linked list” to clarify, as described, for example, in FIGS. 2A and 2B and in page 5, line 10, to page 6, line 13, of the Specification, that the recited queue is a linked list comprising a plurality of Buffer Descriptor Address (BDA) entries.

Applicant similarly amended independent claims 9, 17 and 25, and amended claims 4-8, 12-16, 20-24 and 28-31 to make the language recited therein consistent with the amended language of the respective independent claims. Applicant also corrected typographical errors in

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claims 25 and 27, specifically, replacing the word "causes" appearing in claim 25 with "cause", and replacing the word "method" appearing in the preamble of claim 27 with "system."

Furthermore, applicant added new claims 32-35 which depend from respective independent claims 1, 9, 17 and 25. The new claims recite "receiving network packets; and storing the packets in one or more of the plurality of data buffers," or similar language. Support for the recited features in the new claims is found throughout the application including, for example, at FIG. 1, and page 2, line 6, to page 3, line 8 of the originally filed application.

Enclosed is a Request for Continued Examination. Please apply any other charges to deposit account 06-1050, referencing attorney docket 10559-614001.

Respectfully submitted,

Date:

March 16, 2006

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